Construction of Error-Correction Code Application by Applying Finite Field and Hadamard Matrix Theory

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K A T A  K U N C I
Kode perbaikan-galat
Matriks
Lapangan

A B S T R A K

K E Y W O R D S
Error-correction code
Matrix
Field

A B S T R A C T
In the world of digitals and electronics, information could be easily transferred via communication channel. The transferred data might be or not containing error that rises by variety of causes. To avoid this matter, error-correction code is needed along with its efficient application. Error-correction coding is a concept to add extra data bits to makes the transmission of data or information more robust to disturbances present on the communication channel. One of them is the Random Parity Code (RPC) introduced by Hershey and Tiemann (1996). This article shows the construction of error-correction code application that is made based on RPC concept with the help of finite field and hadamard matrix theory. The application is made using Rapid Application Development (RAD) method. The produced application is in form of computer software. The software become more efficient by applying the divide and conquer algorithm concept.

1. Introduction
Most of people now use computer, people like to use computer because of its advantage to be more efficient and effective for doing any tasks. Another advantage of using computer is, any files (this include information and communication) within the computer could be easily managed and shared or transferred by copying or sending via communication channel such as cable, wireless network, hardware drive or other devices. But, despite of its capability to transfer file easily, the transfer process of files (or information or communication) not always come in handy. The files had to going through a communication channel when it progressing the transfer process. The file resulted from going through this communication channel might be different from the initial file. The transferred file may or may not contain error. Errors from the communication channel are rises by variety of causes such as lightning or weather failure, disc scratches, hardware failure, etc. To avoid this matter, error-correction code was invented.

Error correction code consists of encoding and decoding process. When encoding, the message we decide to encode will be encoded into codeword using
encoding method. This codewords will be sent to go through a channel, this channel assumed to be noisy. The channel could be a cable or wireless network, hardware drive or other devices. When the code received, it may contain errors or perhaps not. The received code then will be decoded. Due to redundancy and decoding method, original message could be recovered. The illustration of communication channel with and without coding process can be seen in Figure 1 and Figure 2.

![Figure 1 – Normal communication channel.](image1)

![Figure 2 – Communication channel with coding.](image2)

In this research an error-correcting code application is made, the theory for making the application based on Random Parity codes of J. Hershey and J. Tiemann introduced in 1996. The Encoding and Decoding concept can be seen in Figure 3 and Figure 4.

![Figure 3 – Random parity encoder (Hershey and Tiemann, 1996).](image3)

![Figure 4 – Random parity decoder (Hershey and Tiemann, 1996).](image4)

In this research, the error-correcting code application will be made by applying some coding theory with the support of matrix, and field theory. In the matrix theory there is Hadamard matrices, a square matrix of order $n$ that is an $n \times n$ matrix with integer entries $\pm 1$ satisfying: $H_nH_n^T = H_n^TH_n = nI$ ; $H_n^T$ is the transpose of $H_n$ and $I$ is the $n \times n$ identity matrix. Hadamard matrices is an application of finite field that have a special form and are named to J. Hadamard, because of the property of their determinants which attain a bound originally given by J. Hadamard in 1893 (Agaian, 1985)

The objectives of this research are to produce an error-correction code application and to see the application efficiency after implemented with divide and conquer algorithm concept.

The results of research hopefully will have some benefits, such as the produced application can be used to avoid error on file transmission, the produced application can be implemented on any devices that require transmission, the produced application can be used as a base design or blueprint to make another error-correction code or even its implementation.

2. Methods

2.1. Software Development Method

The error-correction code application in this research developed using one of the Software Development Method (SDM)). The method used in this research is Rapid Application Development (RAD), developed by James Martin during the 1980s at IBM and finally formalized it by publishing Rapid Application Development book in 1991. The model can be seen in Figure 5.

![Figure 5 – RAD model.](image5)

2.2. Compiler

The compiler used for constructing the application is Borland Delphi 7 (Build 4.453), based on Pascal Programming Language.

3. Results and Discussion

3.1. Planning and Requirements

The target user is the computer user that wanted to avoid corrupted file as a result of the file transfer. This research requires computer plus its operating system and compiler.

3.2. Design

The design separated into design diagram that visualize the model of the program, and design description. The design diagram of the RPC application modeled using Unified Modeling Language (UML).
3.2.1. Design Diagram

The design diagram in UML is represented into two different views: the static (or structural) view and dynamic (or behavioral) view.

**Structural Diagram**

There are 4 types structural diagram for modeling the program, they are:

a. Class Diagram

This diagram describes the structure of RPC program by showing its classes, attributes, operations, and the relationships among the classes, as in Figure 6 to 9.

b. Package Diagram

This diagram describes how a system is split up into logical groupings by showing the dependencies among these groupings, as in Figure 10.

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**Figure 6** – Primary class diagram.

**Figure 7** – Operationsets.pas class diagram.

**Figure 8** – About1.pas class diagram.

**Figure 9** – Confirmation1.pas class diagram.

**Figure 10** – Package Diagram.

**Figure 11** – Deployment diagram.

**Figure 12** – Component diagram.

**Figure 13** – Activity diagram.
c. Deployment Diagram

This diagram describes the hardware used in system implementations and the execution environments and artifacts deployed on the hardware, as in Figure 11.

![Deployment Diagram](image)

**Figure 14 – Use case diagram.**

d. Component Diagram

This diagram describes how random parity code is split up into components and shows the dependencies among these components, as in Figure 12.

![Component Diagram](image)

**Figure 15 – sequence diagram.**

3.2.2. Design Description

The design of the application contain of the program as the main executor and the unit as a part of the main program.

**Program Description**

Main program is the main executor of the application that initializes the application and run all units within it.

**Unit Description**

Units are the individual source code modules that make up a program or in other words it is the constructor of main program. A unit is a place to group functions and procedures that can be called from main program. A unit consists of at least 3 parts such as: unit statement as the identifying part, interface part as the connector part to other unit or program, implementation part as the definer of procedures and functions. In this program 4 unit construct the main program: operationsets, functionsets, confirmation1 and about1. Unit functionsets is the main focus of the research, especially the procedure Random Parity Encode and Random Parity Decode as the implementation of RPC.

```plaintext
procedure RandomParityEncode;
    var
        i, j, k, l:integer;
        tempbits:array[0..7] of byte;
        tmpbyte:byte;
    begin
        makeprojector; bitpartition;
        for i:=0 to ibytesread-1 do
            begin
                for k :=0 to 3 do
                    begin
                        for j:=0 to 7 do
                            begin
                                tempbits[j]:=obits(projector[j],zlocation[i,k]);
                            end;
                            l:=1; tmpbyte:=0;
                            tmpbyte:= tmpbyte or tempbits[0];
                            repeat
                                tmpbyte:=(tmpbyte shl 1) or tempbits[l];l:=l+1;
                            until l=7;
                            bufferin[(i*4)+k]:=tmpbyte;
                    end;
            end;
    end;
```

Behavior Diagram

There are 3 types behavior diagram for modeling the RPC application, they are:

a. Activity Diagram

This diagram describes the operational step-by-step workflows of components in a system. An activity diagram shows the overall flow of control, as in Figure 13.

b. Use Case Diagram

This diagram describes the functionality provided by RPC in terms of actors and their goals that represented as use cases, also any dependencies among them, as in Figure 14.

c. Sequence Diagram

This diagram shows how objects communicate with each other in terms of a sequence of messages. Also indicates the life spans of objects relative to those messages, as in Figure 15.
3.3. Construction

The RPC application was built using Compiler in this case Delphi 7 and program source code based on Pascal Programming Language. The application constructed based on the design that have been explained before.

3.4. Cutover and Finalization

This section describes implementation.

Implementation

a. Changes from the original concept

To show what changes implementations are made to the original concept, the original one is shown first.

1. Original concept:
   i. Binary input for info word is any of the bit size (1-bit to n-bit).
   ii. Projector address x generated with conditions:
      - Pseudo randomly generated.
      - Cannot be a zero or 0.
      - $x \in A$ with element of $A$ from 1 to n-bit data size $- 1$ or $A = \{1,...,2^n-1\}$

Example: 
$n = 2$; $\text{Size}(n) = 2^n = 2^2 = 4; A = \{1,2,3\}$

$n = 8$; $\text{Size}(n) = 2^8 = 256; A = \{1,2,3,...,255\}$

ii. Number of Projector : any (affected by the input, usually at least $2^n + 1$)

b. Changes and implementation:
   - Data type used in program is set to 8-bit
   - Number of bit input for info word is 2-bit in 4 parts, then $n = 2$.
   - Projector addresses generated = \{1, 2, 3\}.
   - Projector generated = $2^{n+1} = 2^3 = 8$

b. Algorithms Implementation

There are some algorithms implemented to the program. Total of 10 pseudocodes of the algorithms can be seen below.
Software Correctness and Efficiency

The correctness verification will be done using experimental analysis. The efficiency will be determined by seeing the program run-time and storage usage capability.

a. Software correctness verification (using experimental analysis)

The process is correct if $d = 0$ for $e \leq 1$, with $d$ = binary distance of encode input and decode output; $e$ = number of error; $n$ = number of letter input.

Tabel 1 – Correctness Table.

<table>
<thead>
<tr>
<th>Input</th>
<th>Output</th>
<th>$n$</th>
<th>$e$</th>
<th>$D$</th>
</tr>
</thead>
<tbody>
<tr>
<td>123</td>
<td>123</td>
<td>3</td>
<td>0</td>
<td>0,0</td>
</tr>
<tr>
<td>123abc</td>
<td>123abc</td>
<td>6</td>
<td>0</td>
<td>0,0,0,0,0,0</td>
</tr>
<tr>
<td>123abc@#</td>
<td>123abc@#</td>
<td>9</td>
<td>0</td>
<td>0,0,0,0,0,0,0,0,0,0</td>
</tr>
<tr>
<td>123</td>
<td>123</td>
<td>3</td>
<td>1</td>
<td>0,0</td>
</tr>
<tr>
<td>123abc</td>
<td>123abc</td>
<td>6</td>
<td>1</td>
<td>0,0,0,0,0,0</td>
</tr>
<tr>
<td>123abc@#</td>
<td>123abc@#</td>
<td>9</td>
<td>1</td>
<td>0,0,0,0,0,0,0,0,0,0,0</td>
</tr>
</tbody>
</table>

According to the Table 1, the whole process is correct because $d = 0$ for $e \leq 1$

b. Software Efficiency

This section shows that the software become more efficient by implementing the Byte Division algorithm than before implementing it, Application A with implementation and Application B without it.

1. Storage/Memory Usage

The memory usage of each application is calculated according to procedures, functions and operations within each of it.

**Tabel 2 – Memory usage of application A and B**

<table>
<thead>
<tr>
<th>Procedure</th>
<th>A</th>
<th>B</th>
</tr>
</thead>
<tbody>
<tr>
<td>Projector</td>
<td>8</td>
<td>512</td>
</tr>
<tr>
<td>Codeword buffer out</td>
<td>4($n$)</td>
<td>64($n$)</td>
</tr>
<tr>
<td>Byte Division</td>
<td>4($n$)</td>
<td>0</td>
</tr>
<tr>
<td>Codeword buffer in</td>
<td>4($n$)</td>
<td>64($n$)</td>
</tr>
<tr>
<td>Byte Set</td>
<td>4($n$)</td>
<td>64($n$)</td>
</tr>
<tr>
<td>Table</td>
<td>4</td>
<td>256</td>
</tr>
<tr>
<td>Result Vector</td>
<td>4</td>
<td>256</td>
</tr>
<tr>
<td>Hadamard Matrix</td>
<td>16</td>
<td>65536</td>
</tr>
<tr>
<td>Total</td>
<td>36+16($n$)</td>
<td>66560+192($n$)</td>
</tr>
<tr>
<td>Initial Memory usage</td>
<td>52</td>
<td>66752</td>
</tr>
</tbody>
</table>

Gambar 1 – Comparison chart of application A and B memory usage growth with n input.

From the Table 2 and chart in Figure 16 can be seen that Application A has less memory usage than Application B even with increasing number of input. Then, App A is better and more efficient than App B. So it is sufficient just to show that implementation of Byte Division algorithm have better run-time and storage/memory usage than without implementing the Byte Division algorithm to the application. In this section the

2. Run-time

Besides the Byte Division algorithm that only implemented to Application A, other algorithms implemented to Application A and B is the same algorithm. Byte Division algorithm makes number of input and step processed by Application A and B different on each following procedures: Buffering, Projector Generator, Encode, Decode, and Hadamard Matrix Construction. With the number of input and step increased, so is the run-time. Because of conditions above the total run-time of each process become:

$$T_A(n) = T_1(n) + T_2(n) + ... + T_d(n) + T_{Comb}(n)$$

$$T_B(n) = T_1(n) + T_2(n) + ... + T_d(n) + T_{Comb}(n)$$
\[ T_A(n), T_B(n) : \text{Application A and B total run-time with } n \text{ input} \]

\[ T_k(n) : \text{run-time of algorithm } k \text{ with } n \text{ input} \]

\[ T_{\text{Comb}}(n) : \text{run time of Byte Division and Byte Set algorithm} \]

\[ n \quad : \text{number of input} \]

It is important to show that Byte Division algorithm have no significant influence to total run-time of application.

From the analysis based on the pseudocode, the run-time for each algorithm \( T_1 \) (Byte Division) and \( T_2 \) (Byte Set), with \( m \) as the number of input for algorithm implementation are:

\[ T_1(n) = 5m(n) \]

\[ T_2(n) = 3m(n) \]

\[ T_{\text{Comb}} = T_1(n) + T_2(n) \]

The value of \( m \) according to implementation on Application A, \( m_1, m_2 = \{4,4\} \) and Application B, \( m_1, m_2 = \{0,64\} \)

\[ T_{\text{Comb}1} = 5m_1n + 3m_2n = 5 \cdot 4 \cdot n + 3 \cdot 4 \cdot n \]

\[ = 20n + 12n = 32n \]

\[ T_{\text{Comb}2} = 5m_1n + 3m_2n = 5 \cdot 0 \cdot n + 3 \cdot 64 \cdot n \]

\[ = 0 + 64n = 64n \]

Then, \( T_{\text{Comb}1} < T_{\text{Comb}2} \) and comparison 1 : 2 for every \( n \), with \( n = 1, 2, \ldots \)

According to Table 3, Application A have less comparison value than Application B, then \( T_A - T_{\text{Comb}1} < T_B - T_{\text{Comb}2} \). Under the condition \( T_{\text{Comb}1} < T_{\text{Comb}2} \), the total run-time condition become \( T_A < T_B \). Because of the linearity of Total run-time equation, the condition works for every \( n \) input.

\( T_A \) is 2 to 64 times more efficient than \( T_B \). Taking the average of comparison value, \( T_A \) is 19.714 times more efficient than \( T_B \).

4. Conclusion

According to results and discussions, some conclusions are made. The application can be produced in form of computer program with some abilities and weaknesses. By applying the Divide and Conquer algorithm concept, the application become more efficient than before applying the concept either in term of memory usage and run-time. In term of memory usage for initial memory usage (52 and 66752), the application is 1283.692 times more efficient. And for each input \( n \), memory usage increased by 16 and 192, the application is 12 times more efficient.

Table 3 – Comparison of application A and B number of step and input.

<table>
<thead>
<tr>
<th>Algorithm implementation</th>
<th>Comparison (A : B)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Projector</td>
<td>8:512 = 1:64</td>
</tr>
<tr>
<td>Random Parity Encode</td>
<td>32:512 = 1:16</td>
</tr>
<tr>
<td>Buffering codeword out</td>
<td>4:64 = 1:16</td>
</tr>
<tr>
<td>Buffering codeword in</td>
<td>4:64 = 1:16</td>
</tr>
<tr>
<td>Hadamard Construction</td>
<td>1:8</td>
</tr>
<tr>
<td>Random Parity Decode</td>
<td>4:64 = 1:16</td>
</tr>
</tbody>
</table>

References
